

*101
Ans.*

is adapted to receive dialing instructions as indicated by block 88 which originate at the portable computer 36 or from some other source. These instructions command the microprocessor to connect the system to a designated telephone number (123-4567) and the received instructions are stored in the microprocessor as shown by block 90. In response to the instructions, the microprocessor then operates to cause the analog switch and conditioning system 32 to complete an instruction path to the cellular interface 30 as taught at 94. With this path completed, the microprocessor operates at 96 to transmit the stored call control signals to the cellular interface with the instructions to cause the cellular interface to send the eight bit parallel control signals required to dial the phone number 123-4567. As a first step in this transmission, the microprocessor checks the state of the cellular bus data lines. If the data lines are in use, the microprocessor enters a loop or waiting mode and does not actuate the AMPS interface and transmit the call control signals to the cellular bus until the data lines are not in use. When the data lines are not in use, the microprocessor will actuate the AMPS interface and transmit the call control signals to the cellular transceiver via the cellular bus. The method by which this first transmission step is implemented will be apparent to those skilled in the art on review of the commented source code implementing this step (located in the "dial.num" routine which is part of the BRIDGSUB routine) in the microfiche Software Appendix. The microprocessor then waits for the call to be answered, and if this does not occur, the NO branch is taken from the decision block 98 to block 100 to exit from the flow chart in FIG. 3. However, if the call is answered, the YES branch is taken from the decision block 98 to block 102, where the microprocessor 34 instructs the analog switch and conditioning system 32 to switch the signal path from the cellular interface to the modem 40. Subsequently, at block 104, the modem is instructed to transmit data received by the microprocessor.--

REMARKS

By this paper, applicant is responding to Examiner Lele's telephone communication of June 3, 2004 noting that the proposed amendment of the paragraph starting at col. 11, line 30 of the original '281 patent (as requested in the Response and